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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/708,368	02/26/2004	CHIAO-JU LIN	10872-US-PA	2367

31561 7590 03/07/2006

JIANQ CHYUN INTELLECTUAL PROPERTY OFFICE  
7 FLOOR-1, NO. 100  
ROOSEVELT ROAD, SECTION 2  
TAIPEI, 100  
TAIWAN

EXAMINER

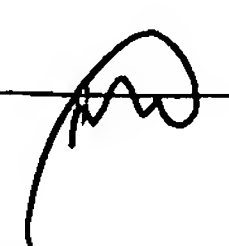
KEANEY, ELIZABETH MARIE

ART UNIT	PAPER NUMBER
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2882

DATE MAILED: 03/07/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 10/708,368	<b>Applicant(s)</b> LIN, CHIAO-JU	
	<b>Examiner</b> Elizabeth Keaney	<b>Art Unit</b> 2882	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 05 January 2006.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-10 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 2/26/04 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Election/Restrictions***

Applicant's election without traverse of Group I in the reply filed on 5 January 2006 is acknowledged.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

**Claims 1-10 are rejected under 35 U.S.C. 102(e) as being anticipated by Park et al. (US Patent 6,771,328; hereinafter Park).**

Re claims 1 and 6: Park discloses, in figure 4 and throughout the disclosure, an active-matrix organic electroluminescent display panel (OEL), comprising:

- a substrate (100);

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- a transparent conductive layer (114) on the substrate;
- a first passivation layer (118) on the transparent conductive layer, having a plurality of openings therein exposing portions of the transparent conductive layer, wherein each opening defines a pixel region (Del);
- a plurality of thin film transistors (T) arranged as a matrix, each comprising a gate electrode (108), a source (124) and a drain (126) disposed on the first passivation layer corresponding to an opening;
- a plurality of organic function layers (132) disposed on the transparent conductive layer in the openings; and
- a plurality of metal electrode layers (134) disposed on the organic function layers and electrically connected to the corresponding drains.

The Examiner notes that the insulating layer (118) has been interpreted to be a passivation layer.

Re claim 10: Park discloses, in figure 4 and throughout the disclosure, an OEL further comprising a metal layer (104) on the substrate, having a plurality of openings therein exposing portions of the substrate.

Re claims 2 and 7: Park discloses each organic function layer comprises a hole injection layer, a hole transport layer, an emitting layer and an electron transporting layer that are stacked sequentially (column 14, lines 40-43).

Re claims 3 and 8: Park discloses, in figure 4 and throughout the disclosure, a second passivation layer (128) disposed on each thin film transistor.

Re claims 4 and 9: Park discloses the transparent conductive layer includes ITO (column 9, line 22).

Re claims 5 and 10: Park discloses each metal electrode layer includes a LiF/Al composite layer (column 10, line 15).

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Elizabeth Keaney whose telephone number is (571)272-2489. The examiner can normally be reached on Monday, Tuesday, Thursday, Friday 7:30-6:00.

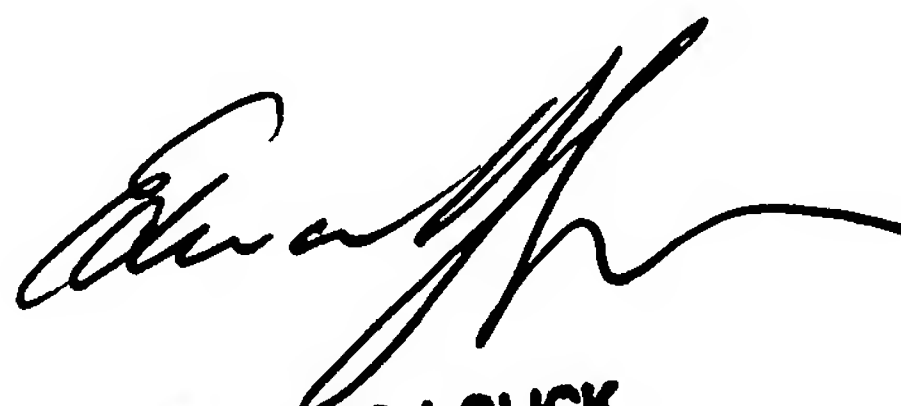
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ed Glick can be reached on (571)272-2490. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Elizabeth Keaney  
Examiner  
Art Unit 2882



**EDWARD J. GLICK**  
**SUPERVISORY PATENT EXAMINER**